

~~WHAT IS CLAIMED IS:~~

1 A wireless communication terminal comprising:
 wireless transmitting/receiving means which transmits
 and receives data;

5 detecting means detecting a receiving electric field
 strength at said wireless transmitting/receiving
 means; and
 operation clock control means controlling a frequency
 of an operation clock for processing data transmitted
 and received by said wireless transmitting/receiving
 means based on a receiving electric field strength
 detected by said detected means.

10 2 The wireless communication terminal according to said
 claim 1, wherein said operation clock control means has
 means controlling a frequency of an operation clock to be
 smaller as a receiving electric field strength becomes
 smaller.

15 3 The wireless communication terminal according to said
 claim 1, wherein said detecting means has memory means
 memorizing a measured value of a detected receiving
 electric field strength, and
 said operation clock control means has means controlling
 a frequency of said operation clock based on a receiving
 electric field strength memorized by said memory means.

20 4 The wireless communication terminal according to said

claim 1, wherein a predetermined number of receiving level inferential values is stored in advance and said detecting means has means for selecting said receiving level inferential value from said previously stored receiving

5 level inferential values corresponding to a detected receiving electric field strength and maintaining said receiving level inferential value, and

10 said operation clock control means has a predetermined number of operation clock generation means generating operation clocks corresponding to said receiving level inferential values and selecting means selecting said operation clock generation means corresponding to a receiving level inferential value maintained at said detecting means.

15 5 The wireless communication terminal according to said claim 1, wherein said wireless transmitting/receiving means is means performing transmitting/receiving processing in accordance with Time Division Multiplexing Access (TDMA) communication method, and

20 said operation clock control means is means controlling a frequency of an operation clock by its being synchronized with a timing of time division receiving operation of said wireless transmitting/receiving section.

25 6 The wireless communication terminal according to said claim 5, wherein said wireless transmitting/receiving

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means has means generating an interrupt signal at a starting time of a receiving slot that is a timing of time division receiving operation and an interrupt end signal at an ending time of said receiving slot, and

said operation clock control means has means controlling an operation clock to be a frequency according to said receiving electric field strength in response to said interrupt signal and ending off controlling a frequency of said operation clock in response to said interrupt end signal.

7 A wireless communication terminal comprising:

wireless transmitting/receiving means generating an interrupt signal at a starting time of a receiving slot that is a timing of time division receiving operation and an interrupt end signal at an ending time of said receiving slot as well as transmitting and receiving data;

detecting means detecting a receiving electric field strength at said wireless transmitting/receiving means and selecting said receiving level inferential value corresponding to said detected receiving electric field strength from a predetermined number of receiving level inferential values stored in advance and maintaining said receiving level inferential value:

5 a predetermined number of operation clock generation means generating operation clocks corresponding to said receiving level inferential values; selecting means selecting operation clock generation means corresponding to a receiving level inferential value maintained at said detecting means out of said predetermined number of operation clock generation means in response to said interrupt signal and selecting an initially selected operation clock generation means in response to said interrupt end signal; and

10 data processing means processing data transmitted and received by said wireless transmitting/receiving means based on an operation clock of operation clock generation means selected by said selecting means.

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8 In a wireless communication terminal, a method of controlling an operation clock for processing transmitting/receiving data, said method comprising the steps of:

20 detecting a receiving electric field strength; and controlling a frequency of an operation clock for processing transmitting/receiving data based on said detected receiving electric field strength.

9 The method of controlling an operation clock for processing transmitting/receiving data according to said

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~~claim 8, wherein said control step is a step of controlling a frequency of an operation clock to be smaller as a detected receiving electric field strength becomes smaller.~~

5 10 The method of controlling an operation clock for processing transmitting/receiving data according to said claim 8, wherein said detecting step, in which a plurality of receiving level inferential values are set in advance, is a step of selecting a receiving level inferential value corresponding to a detected receiving electric field strength out of said plurality of receiving level inferential values, and

15 10 said control step, in which a plurality of operation clocks corresponding to a plurality of receiving level inferential values are prepared in advance, is a step of selecting an operation clock corresponding to said selected receiving level inferential value out of said plurality of operation clocks.

20 11 The method of controlling an operation clock for processing transmitting/receiving data according to said claim 8, wherein transmitting/receiving data is performed in accordance with Time Division Multiplexing Access (TDMA) communication method, and

25 11 said control step is a step of controlling a frequency of said operation clock by its being synchronized with a

timing of time division receiving operation.

12 The method of controlling an operation clock for processing transmitting/receiving data according to said claim 11, wherein said control step is a step of starting controlling an operation clock at a starting time of a receiving slot and ending off controlling a frequency of said operation clock at an ending time of said receiving slot.

13 The method of controlling an operation clock for processing transmitting/receiving data according to said claim 12, wherein said control step is a step of returning a frequency of an operation clock to a certain original frequency of said operation clock at an ending time of said receiving slot.

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